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EXAMINER	
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ART UNIT	PAPER NUMBER
2192	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	12/18/2006	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/611,558

Applicant(s)

ZILAVY, DANIEL V.

Examiner

Isaac T. Tecklu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 July 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-66 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-66 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>06/05/2003, 07/01/2003</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This action is responsive to the application filed on 07/01/2003.
2. Claims 1-66 have been examined.

Oath/Declaration

3. The office acknowledges receipt of a properly signed oath/declaration filed on 07/01/2003.

Claim Objections

4. Claims 17 and 37 are objected to because of the following informalities:

Claim 17 depends on claim 2 rather than claim 12. Appropriate correction is required.

Claim 37 depends on claim 24 rather than claim 36. Appropriate correction is required.

Claim Rejections - 35 USC § 101

5. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.
6. Claims 1-43 are rejected under 35 U.S.C 101 because the claimed invention is directed to non-statutory matter.

The Federal Circuit has recently applied the practical application test in determining whether the claimed subject matter is statutory under 35 U.S.C. § 101. The practical application test requires that a “useful, concrete, and tangible result” be accomplished. An “abstract idea” when practically applied is eligible for a patent. As a consequence, an invention, which is eligible for patenting under 35 U.S.C. § 101, is in the “useful arts” when it is a machine, manufacture, process or composition of matter, which produces a concrete, tangible, and useful result. The test for practical application is thus to determine whether the claimed invention produces a “useful, concrete and tangible result”.
7. Claims 1, 12, 18, 24, 30, 35, 39 and 42 recite determining whether the first FPU code is compatible with the computer system; and if the first FPU code is determined not to be compatible with the computer system, notifying a user of the computer system of the

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incompatibility. As a whole, the claims lack teaching as to what is being transformed or what action is taken as a result of the notifying a user of the incompatibility. That is, comprising solely of steps of determining and notifying, the claims do not enable the realization of a concrete result because what result being inferred from said determining and notifying remains a concept or a non-tangible representation that cannot materialize itself out and into a tangible outcome without teaching from the claim for conveying that an explicit action is executed to yield a result based upon such said determining and notifying steps. In addition, the claims lack to teach tangible result if the FPU is determined to be compatible. Absent any tangible result, the claimed invention thus fails to fulfill the Practical Test Application; and is rejected for leading to a non-statutory subject matter.

Claims 2-7, 9, 13-17, 19-23, 25-29, 31-33, 36-37, 40 and 43 are rejected for failing to cure the deficiencies of the above rejected non-statutory claims 1, 12, 18, 24, 30, 35, 39 and 42 above.

Claim Rejections - 35 USC § 102

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

9. Claims 1-66 are rejected under 35 U.S.C. 102(a) as being anticipated by Maffezzoni et al. (US 6,532,535 B1).

Per claim 1, Maffezzoni discloses in a computer system including a first field-programmable unit (FPU) code (e.g. FIG. 10B and related text), a computer-implemented method comprising steps of:

(A) determining whether the first FPU code is compatible with the computer system (e.g. FIG. 15, element 666 and related text); and

(B) if the first FPU code is determined not to be compatible with the computer system, notifying a user of the computer system of the incompatibility (e.g. FIG. 15, element 670 and related text).

Per claim 2, Maffezzoni discloses the method of claim 1, wherein the computer system further comprises a plurality of field-programmable units including a corresponding plurality of FPU codes (e.g. FIG. 5I, elements 964-968 and related text), and wherein the step (A) comprises a step of:

(A) (1) determining whether the first FPU code is compatible with the plurality of FPU codes (col. 49: 25-30 "... determined whether the new hard drive is compatible ...").

Per claim 3, Maffezzoni discloses the method of claim 2, wherein the computer system further comprises a plurality of field-replaceable units, and wherein the step (A) further comprises a step of:

(A) (2) determining whether the first FPU code is compatible with the plurality of field-replaceable units (e.g. FIG. 5D, element 926 and related text).

Per claim 4, Maffezzoni discloses the method of claim 2, wherein the computer system further comprises a revision compatibility descriptor identifying a plurality of compatible combinations of field-programmable unit codes (e.g. FIG. 5G, element 954a and related text), and wherein the step (A)(1) comprises a step of determining that the first FPU code is compatible with the plurality of FPU codes if a combination of the first FPU code (col. 49: 25-30 "... determined whether the new hard drive is compatible ...") and the plurality of FPU codes is among the plurality of compatible combinations of field-programmable unit codes identified by the revision compatibility descriptor (e.g. FIG. 15, element 666 and related text).

Per claim 5, Maffezzoni discloses the method of claim 2, wherein the computer system further comprises a plurality of field-replaceable units and a revision compatibility descriptor identifying a plurality of compatible combinations of field-programmable unit codes and field-replaceable units (e.g. FIG. 5G, element 954a and related text), and wherein the step (A)

comprises a step of determining that the first FPU code is compatible with the computer system if a combination of the first FPU code, the plurality of FPU codes (e.g. FIG. 15, element 666 and related text), and the plurality of field-replaceable units is among the plurality of combination combinations of field-programmable unit codes and field-replaceable units identified by the revision compatibility descriptor (col. 49: 25-30 "... determined whether the new hard drive is compatible ...").

Per claim 6, Maffezzoni discloses the method of claim 1, wherein the first field-programmable unit comprises a field-replaceable unit (e.g. FIG. 5I, element 964 and related text).

Per claim 7, Maffezzoni discloses the method of claim 1, wherein the step (A) is performed in response to installation of the first field-programmable unit in the computer system (col. 49: 25-30 "... new hard drive has been installed ...").

Per claim 8, Maffezzoni discloses the method of claim 1, wherein the step (B) comprises a step of:

(B) (1) providing the user with information descriptive of second FPU code that is suitable for storage in the first field-programmable unit and that is compatible with the computer system (e.g. FIG. 15, element 670 and related text).

Per claim 9, Maffezzoni discloses the method of claim 8, wherein the step (A) is performed in response to replacement of a second field-programmable unit with the first field-programmable unit, and wherein the second field-programmable unit includes the second FPU code (e.g. FIG. 15, element 662 and related text).

Per claim 10, Maffezzoni discloses the method of claim 8, and wherein the step (B)(1) comprises steps of:

(B) (1) (a) identifying a compatible combination of field-programmable unit codes previously installed in the computer system (e.g. FIG. 5D, element 924 and related text);

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(B) (1) (b) identifying, in the identified combination of previously-installed field-programmable unit codes, an identifier of FPU code suitable for installation in the first field-programmable unit (e.g. FIG. 5D, element 926 and related text); and

(B) (1) (c) providing the user with information descriptive of the FPU code identified by the identifier (e.g. FIG. 15, element 670 and related text).

Per claim 11, Maffezzoni discloses the method of claim 8, wherein the computer system further comprises a plurality of FPU's including a corresponding plurality of FPU codes, and a revision compatibility descriptor identifying a plurality of combinations of compatible field-programmable unit codes, and wherein the step (B)(1) comprises steps of:

(B) (1) (a) identifying, in the revision compatibility descriptor, a record describing the plurality of FPU codes (e.g. FIG. 5C, element 910 and related text);

(B) (1) (b) identifying, in the identified record, a code identifier identifying FPU code suitable for use in the first field-programmable unit (e.g. FIG. 5C, element 912 and related text); and

(B) (1) (c) providing the user with information descriptive of the FPU code identified by the code identifier (e.g. FIG. 5C, element 914 and related text).

Per claim 12, this is the apparatus version of the claimed method discussed above (Claim 1), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 13, this is the apparatus version of the claimed method discussed above (Claim 2), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 14, this is the apparatus version of the claimed method discussed above (Claim 3), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 15, this is the apparatus version of the claimed method discussed above (Claim 4), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 16, this is the apparatus version of the claimed method discussed above (Claim 5), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 17, this is the apparatus version of the claimed method discussed above (Claim 7), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 18, this is the storage version of the claimed method discussed above (Claim 1), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 19, this is the storage version of the claimed method discussed above (Claim 2), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 20, this is the storage version of the claimed method discussed above (Claim 3), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 21, this is the storage version of the claimed method discussed above (Claim 4), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 22, this is the storage version of the claimed method discussed above (Claim 5), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 23, this is the storage version of the claimed method discussed above (Claim 6), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 24, this is another system version of the claimed method discussed above (Claim 1), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 25, this is another system version of the claimed method discussed above (Claim 2), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 26, this is another system version of the claimed method discussed above (Claim 3), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 27, this is another system version of the claimed method discussed above (Claim 4), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 28, this is another system version of the claimed method discussed above (Claim 5), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

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Per claim 29, this is another system version of the claimed method discussed above (Claim 6), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 30, Maffezzoni discloses in a computer system including a first field-programmable unit including first FPU code and a second field-programmable unit including second FPU code, a computer-implemented method comprising steps of:

(A) after replacement of the second field-programmable unit by the first field-programmable unit, determining whether the first FPU code is different from the second FPU code (col. 49: 25-30 "... determined whether the new hard drive is compatible ..."); and

(B) notifying a user of the computer system that the first field-programmable unit is incompatible with the computer system if it is determined that the first FPU code is different from the second FPU code (e.g. FIG. 15, element 670 and related text).

Per claim 31, Maffezzoni discloses the method of claim 30, wherein the first field-programmable unit comprises a field-replaceable unit (e.g. FIG. 5I, elements 964-968 and related text).

Per claim 32, Maffezzoni discloses the method of claim 30, wherein the step (B) comprises a step of:

(B) (1) providing the user with information descriptive of third FPU code that is suitable for storage in the first field-programmable unit and that is compatible with the computer system (e.g. FIG. 15, element 670 and related text).

Per claim 33, Maffezzoni discloses the method of claim 32, wherein the third FPU code comprises the second FPU code (e.g. FIG. 15, element 662 and related text).

Per claim 34, Maffezzoni discloses the method of claim 32, wherein the step (B)(1) comprises steps of:

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(B) (1) (a) identifying a combination of compatible field-programmable unit codes previously installed in the computer system (e.g. FIG. 5D, element 924 and related text);

(B) (1) (b) identifying, in the identified combination of previously-installed field-programmable unit codes, an identifier of the second FPU code (e.g. FIG. 5D, element 926 and related text); and

(B) (1) (c) providing the user with information descriptive of the second FPU code (e.g. FIG. 15, element 670 and related text).

Per claim 35, this is another system version of the claimed method discussed above (Claim 30), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 36, this is another system version of the claimed method discussed above (Claim 31), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 37, this is another system version of the claimed method discussed above (Claim 33), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 38, this is another system version of the claimed method discussed above (Claim 34), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 39, Maffezzoni discloses a storage medium readable by a computer in a computer system including a first field-programmable unit (FPU) and a second FPU, the first FPU including first FPU code, the second FPU including second FPU code, the storage medium tangibly embodying program instructions executable by the computer to perform method steps of:

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(A) after replacement of the second FPU by the first FPU, determining whether the first FPU code is different from the second FPU code (e.g. FIG. 15, element 666 and related text); and

(B) notifying a user of the computer system that the first FPU is incompatible with the computer system if it is determined that the first FPU code is different from the second FPU code (e.g. FIG. 15, element 670 and related text).

Per claim 40, Maffezzoni discloses the storage medium of claim 39, wherein the first FPU comprises a field-replaceable unit (e.g. FIG. 5D, element 926 and related text).

Per claim 41, Maffezzoni discloses the storage medium of claim 39, wherein the step (B) comprises a step of:

(B) (1) providing the user with information descriptive of third FPU code that is suitable for storage in the first FPU and that is compatible with the computer system (e.g. FIG. 15, element 670 and related text).

Per claim 42, this is system version of the claimed storage discussed above (Claim 39), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 43, this is system version of the claimed storage discussed above (Claim 40), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 44, this is system version of the claimed storage discussed above (Claim 41), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 45, Maffezzoni discloses in a computer system including a first field-programmable unit including first FPU code (e.g. FIG. 10B and related text), a computer-implemented method comprising steps of:

(A) determining whether the first FPU code is compatible with the computer system (e.g. FIG. 15, element 666 and related text);

(B) if the first FPU code is determined not to be compatible with the computer system, identifying second FPU code that is compatible with the computer system and suitable for installation in the first field-programmable unit (e.g. FIG. 15, element 670 and related text); and

(C) installing the second FPU code in the first field-programmable unit (col. 44: 1-7).

Per claim 46, Maffezzoni discloses the method of claim 45, wherein the computer system further comprises a plurality of field-programmable units including a corresponding plurality of FPU codes, and wherein the step (A) comprises a step of:

(A) (1) determining whether the first FPU code is compatible with the plurality of FPU codes (col. 49: 25-30 "... determined whether the new hard drive is compatible ...").

Per claim 47, Maffezzoni discloses the method of claim 46, wherein the computer system further comprises a plurality of field-replaceable units, and wherein the step (A) further comprises a step of:

(A) (2) determining whether the first FPU code is compatible with the plurality of field-replaceable units (e.g. FIG. 5D, element 926 and related text).

Per claim 48, Maffezzoni discloses the method of claim 46, wherein the computer system further comprises a revision compatibility descriptor identifying a plurality of compatible combinations of field-programmable unit codes (e.g. FIG. 5G, element 954a and related text), and wherein the step (A)(1) comprises a step of determining that the first FPU code is compatible with the plurality of FPU codes if a combination of the first FPU code (col. 49: 25-30 "... determined whether the new hard drive is compatible ...") and the plurality of FPU codes is among the plurality of compatible combinations of field-programmable unit codes identified by the revision compatibility descriptor (e.g. FIG. 15, element 666 and related text).

wherein the computer system further comprises a plurality of field-replaceable units and a revision compatibility descriptor identifying a plurality of compatible combinations of field-programmable unit codes and field-replaceable units (e.g. FIG. 5G, element 954a and related text), and wherein the step (A) comprises a step of determining that the first FPU code is compatible with the computer system if a combination of the first FPU code, the plurality of FPU codes (e.g. FIG. 15, element 666 and related text), and the plurality of field-replaceable units is among the plurality of combination combinations of field-programmable unit codes and field-replaceable units identified by the revision compatibility descriptor (col. 49: 25-30 "... determined whether the new hard drive is compatible ...").

Per claim 50, Maffezzoni discloses the method of claim 45, wherein the first field-programmable unit comprises a field-replaceable unit (e.g. FIG. 5I, element 964 and related text).

Per claim 51, Maffezzoni discloses the method of claim 45, wherein the step (A) is performed in response to installation of the first field-programmable unit in the computer system (col. 49: 25-30 "... new hard drive has been installed ..." and col. 44: 1-7).

Per claim 52, this is the apparatus version of the claimed method discussed above (Claim 45), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 53, this is the apparatus version of the claimed method discussed above (Claim 46), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 54, this is the apparatus version of the claimed method discussed above (Claim 47), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 55, this is the apparatus version of the claimed method discussed above (Claim 48), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 56, this is the apparatus version of the claimed method discussed above (Claim 49), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 57, this is the storage version of the claimed method discussed above (Claim 45), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 58, this is the storage version of the claimed method discussed above (Claim 46), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 59, this is the storage version of the claimed method discussed above (Claim 47), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 60, this is the storage version of the claimed method discussed above (Claim 48), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 61, this is the storage version of the claimed method discussed above (Claim 49), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 62, this is the computer system version of the claimed method discussed above (Claim 45), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 63, this is the computer system version of the claimed method discussed above (Claim 46), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 64, this is the computer system version of the claimed method discussed above (Claim 47), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 65, this is the computer system version of the claimed method discussed above (Claim 48), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

Per claim 66, this is the computer system version of the claimed method discussed above (Claim 49), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Maffezzoni.

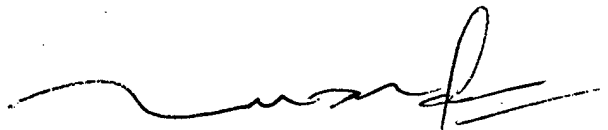
Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Isaac T. Tecklu whose telephone number is (571) 272-7957. The examiner can normally be reached on M-TH 9:300A - 8:00P.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Isaac Tecklu
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TUAN DAM
SUPERVISORY PATENT EXAMINER